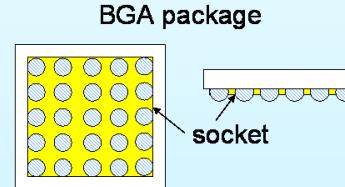
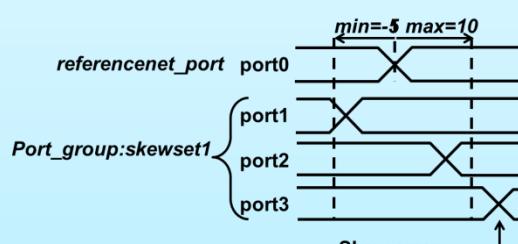


# LPB Standard Format Abstract

## Component (C-Format)

Abstract	<p>Information of the parts that includes</p> <ul style="list-style-type: none"><li>- Pin assignment</li><li>- Design constraint</li><li>- Design Status</li><li>- JEITA original format using XML</li></ul>  
Example	<pre>&lt;module name="SOC_PKG" type="PKG" shape_id="PKG_BODY" &gt;   &lt;socket name="SOC_PKG" &gt;     &lt;port id="A5"      x="-8500"      y="12500"      angle="0"      name="FKBDO[5]" /&gt;     &lt;port id="A6"      x="-7500"      y="12500"      angle="0"      name="FKBDO[2]" /&gt;     &lt;constraint&gt;       &lt;impedance group_name="FKB_DIN" type="single" min="40" typ="50" max="60"/&gt;       &lt;delay      group_name="FKB_DIN" min="100" typ="150" max="200" /&gt;     &lt;/constraint&gt;   &lt;/socket&gt; &lt;/module&gt;</pre>